Amendments to the Abstract

Please **add** the following Abstract to this application.

-- Supportably mounted by each chassis ~~(102c, 104c)~~ of a multi-chassis broadcast router ~~(100)~~ are primary router matrix cards ~~(102a, 104a)~~, redundant router matrix cards ~~(102b, 104b)~~ and clock-demanding input and output cards ~~(136-1 through 136-N and 138-1 through 138-M, 142-1 through 142-N and 144-1 through 144-M)~~. A first master clock ~~(134)~~ resides on the primary router matrix card ~~(102a)~~ of a first chassis ~~(102c)~~ while a second master clock ~~(154)~~ resides on the redundant router matrix card ~~(104b)~~ of a second chassis ~~(104c)~~. Each master clock ~~(134, 154)~~ is configured to provide a respective common clock signal to all of the input and output cards ~~(136-1 through 136-N and 138-1 through 138-M, 142-1 through 142-N and 144-1 through 144-M)~~ of the first and second chassis ~~(102c and 104c)~~. Control logic ~~(148, 156)~~ determines whether the first master clock ~~(134)~~ or the second master clock ~~(154)~~ issues the common clock signal. --